

Model-Assisted Analysis of Intermediate Resistive State Switching in Wafer-Scale Vertical MoS₂ Memristive Devices

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Wafer-scale Ag/2D-MoS₂/Pd memristors based on MOCVD-grown multilayer MoS₂ exhibit volatile and nonvolatile switching and show a robust intermediate resistive state (IRS) across the wafer. While the experiments reveal stable IRS behavior, set and reset voltages within ± 1 V, endurance beyond 2500 cycles, retention exceeding 10^6 s, and a yield of 98%, the origin of the IRS and its transition to the low resistive state (LRS) requires a mechanistic interpretation beyond purely electrical characterization. In this work, we therefore focus on model-assisted analysis of the switching dynamics. Two complementary electrochemical metallization (ECM) modeling approaches were applied to the measured current-voltage characteristics: an established 1D compact model and a 2D axisymmetric continuum model. The 1D model includes interfacial redox reactions, ionic hopping transport, tunneling current, and vertical filament growth with constant radius. It reproduces the transition from the high resistive state to the IRS, but fails to capture the subsequent IRS \leftrightarrow LRS switching because the filament reaches a minimum gap and further resistance reduction is suppressed. In contrast, the 2D continuum model additionally includes arbitrary conductive filament growth, active electrode dissolution, tunnel-barrier heating, and mechanically induced stress. This approach reproduces the gradual IRS \leftrightarrow LRS characteristics and explains the experimentally observed variability through physically motivated side growth. The simulations support a switching picture in which a vertical remnant filament first stabilizes the IRS, followed by lateral Ag filament growth along van der Waals gaps in layered MoS₂, which drives the transition toward the LRS. The combined experimental and simulation results thus identify anisotropic filament growth in a layered material system as the key mechanism governing multistate switching and synaptic functionality in these vertical memristive devices.

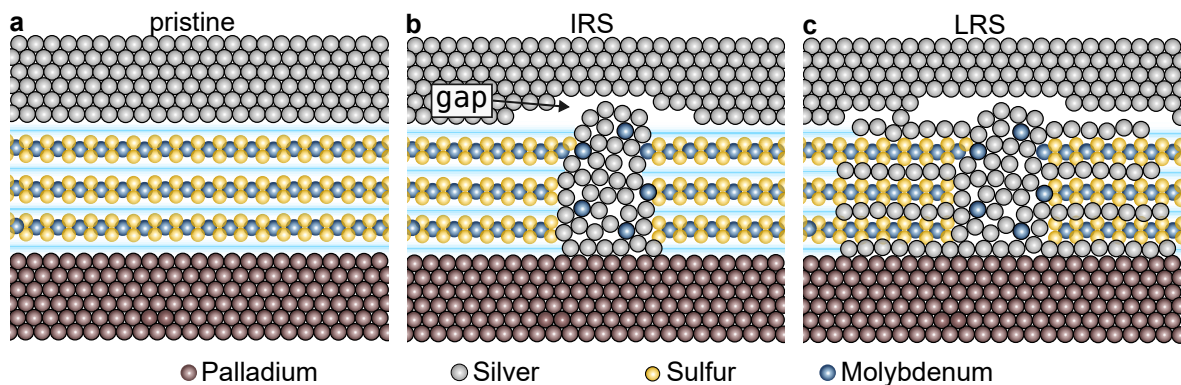


Figure 1: Schematic illustration of the 2D-MoS₂ ECM device in (a) pristine HRS, (b) IRS and (c) LRS. Adapted from [1], licensed under CC BY 4.0.

References

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